

60V N-Channel MOSFET

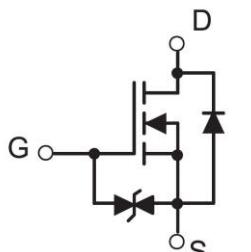
Description

The PME60N03U uses advanced Trench technology and designs to provide excellent $R_{DS(ON)}$ with low gate charge. This device is suitable for use in PWM, load switching and general purpose applications.

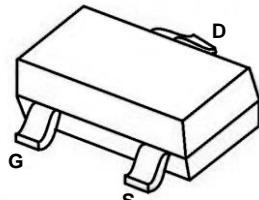
Features

- Super high density cell design for extremely low $R_{DS(ON)}$
- Exceptional on-resistance and maximum DC current capability
- Capable doing Cu wire bonding
- ESD protected Gate HBM 2KV

Dimensions and Pin Configuration



Circuit diagram

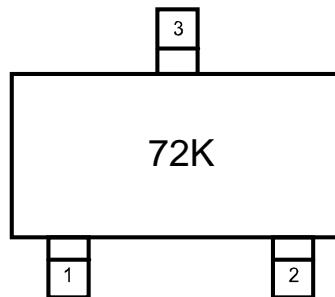


SOT-323

Applications

- Power Management in Note book
- Portable Equipment
- Battery Powered System

Marking Information



72K = Device Marking Code

MOSFET Product Summary

$V_{(BR)DSS}$	$R_{DS(on)} \text{ MAX}$	I_D
60V	3Ω@10V	340mA
	4Ω@4.5V	
	4.5Ω@3V	

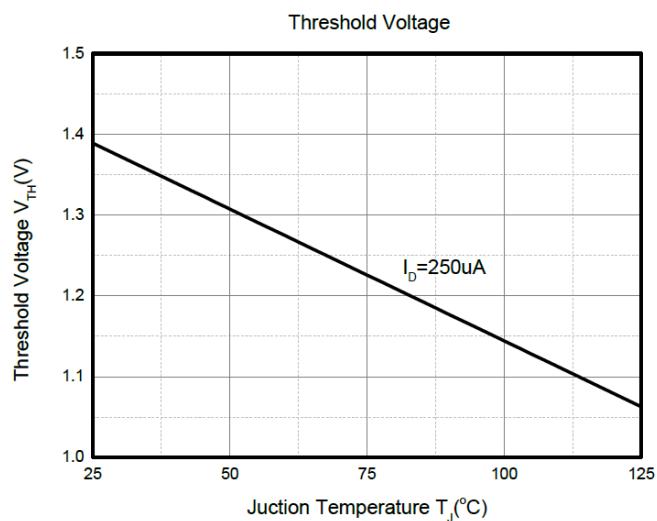
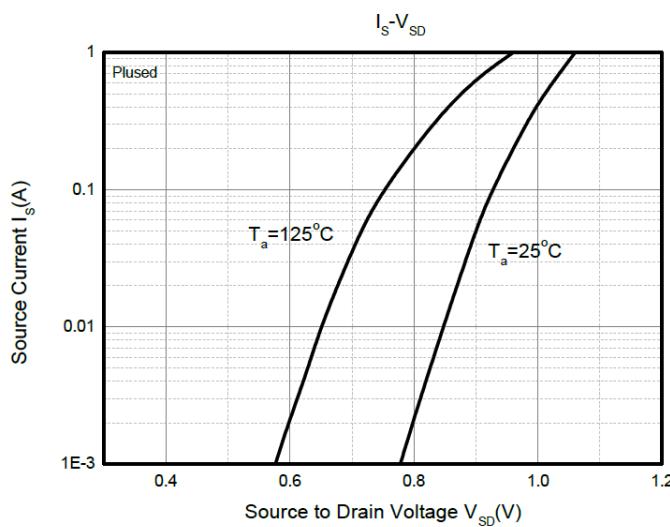
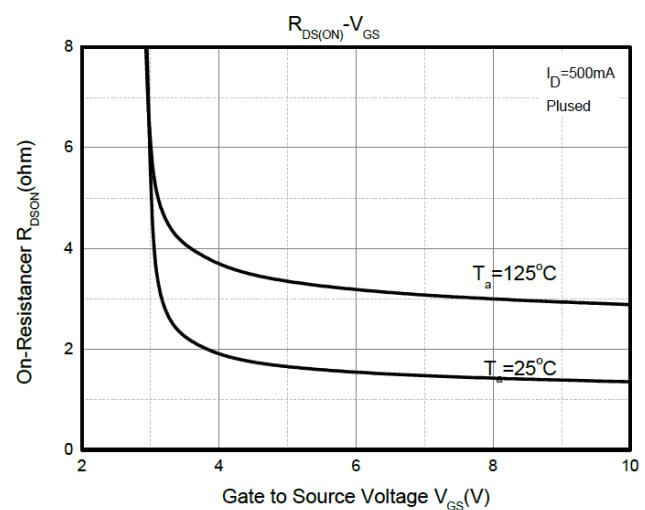
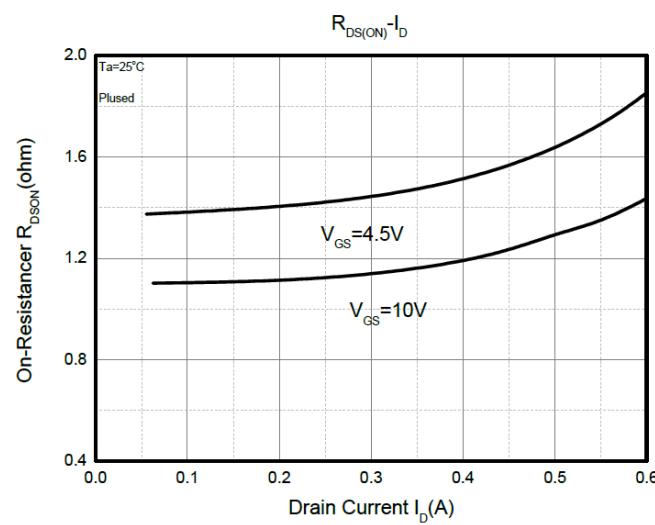
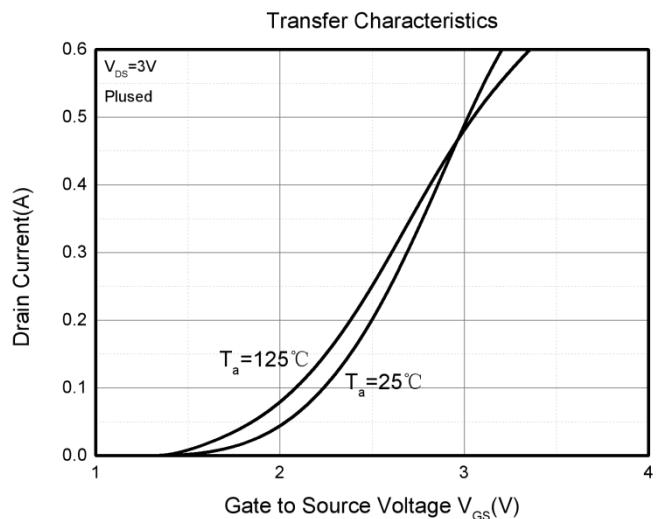
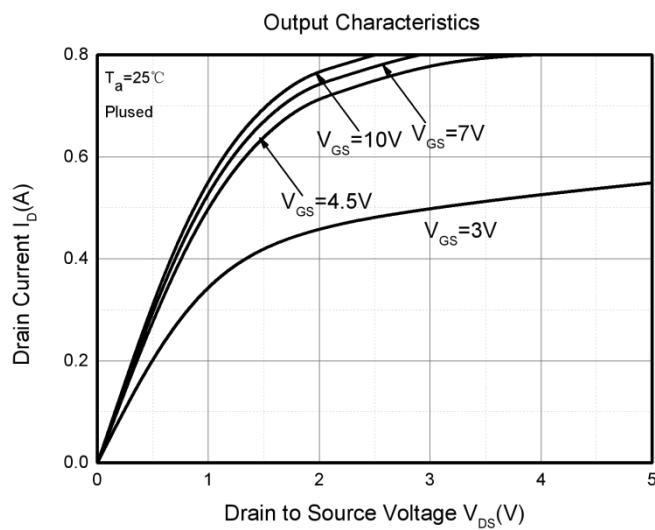
Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise noted)

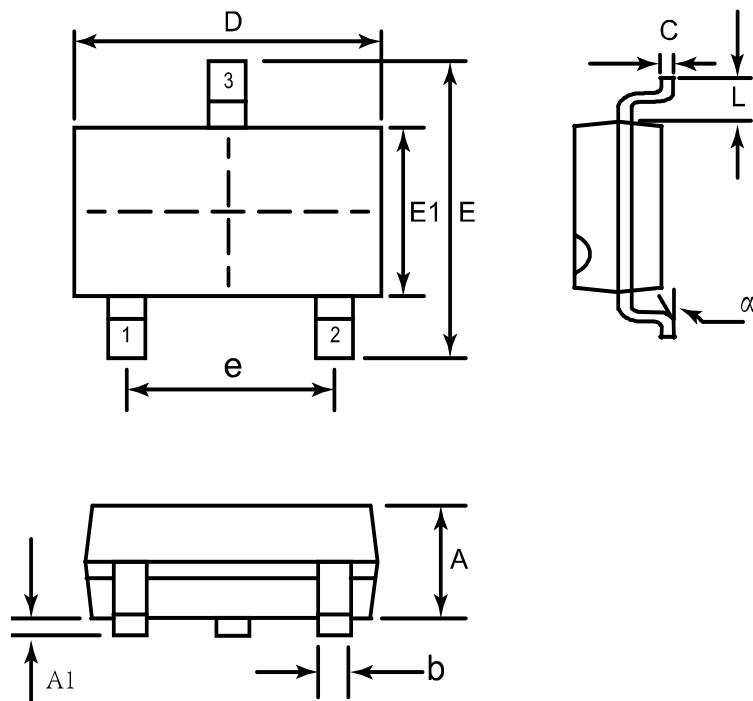
Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	±20	V
Continuous Drain Current	I_D	340	mA
Plused Drain Current ¹	I_{DM}	800	mA
Power Dissipation	P_D	0.2	W
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	625	°C/W
Junction Temperature	T_J	150	°C
Storage Temperature	T_{STG}	-55~ +150	°C

Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Static Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	60			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	1	1.5	2.5	V
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm 20\text{V}$			± 10	μA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}}=60\text{V}, V_{\text{GS}}=0\text{V}$			1	μA
Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=500\text{mA}$		1.3	3	Ω
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=200\text{mA}$		1.8	4	
		$V_{\text{GS}}=3\text{V}, I_{\text{D}}=10\text{mA}$			4.5	
Diode Forward Voltage	V_{SD}	$I_{\text{S}}=200\text{mA}, V_{\text{GS}}=0\text{V}$		0.82	1.3	V
Dynamic characteristics						
Total Gate Charge	Q_g	$V_{\text{DS}}=15\text{V}, V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=200\text{mA}$		1.5		nC
Gate-Source Charge	Q_{gs}			1.9		
Gate-Drain Charge	Q_{gd}			0.4		
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$		28		pF
Output Capacitance	C_{oss}			9		
Reverse Transfer Capacitance	C_{rss}			2		
Switching Characteristics						
Turn-On Delay Time	$t_{\text{d(on)}}$	$V_{\text{DD}}=30\text{V}, R_{\text{L}}=150\Omega$ $I_{\text{D}}=200\text{mA}, V_{\text{GEN}}=10\text{V}, R_{\text{G}}=10\Omega$		8.5		ns
Turn-On Rise Time	t_{r}			6		
Turn-Off Delay Time	$t_{\text{d(off)}}$			31.8		
Turn-Off Fall Time	t_{f}			15.5		

Typical Characteristics



SOT-323 Package Outline Drawing

Symbol	Dimensions In Millimeters	
	Min.	Max.
A1	0.00	0.10
A	0.90	1.00
b	0.30	0.50
c	0.10	0.15
D	2.00	2.20
E1	1.15	1.35
E	2.15	2.40
e	1.20	1.40
L	0.525 REF.	
θ	0°	8°