

## 1-Line, Bi-directional, Ultra-low Capacitance

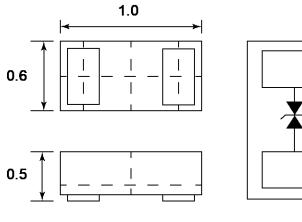
#### Description

PESDR3311P1A is an ultra-low capacitance TVS (Transient Voltage Suppressor) designed to protect high speed data interfaces. It has been specifically designed to protect sensitive electronic components which are connected to data and transmission lines from overstress caused by ESD (Electrostatic Discharge). PESDR3311P1A may be used to provide ESD protection up to ±15KV air and ±15KV contact discharge according to IEC61000-4-2, and withstand peak pulse current up to 4A (8/20µs) according to IEC61000-4-5.

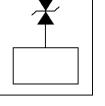
### Features

- Ultra small package: 1.0x0.6x0.5mm
- Protects one data or power line •
- Low operating voltage: 3.3V
- 2-pin leadless package
- Complies with following standards:
  - -IEC 61000-4-2 (ESD) immunity test
    - Air discharge: ±15kV
    - Contact discharge: ±15kV
  - -IEC61000-4-5 (Lightning)3A (8/20µs)
- **RoHS** Compliant

# **Dimensions and Pin Configuration**



Package Dimensions



**Circuit and Pin Schematic** 

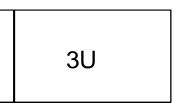
### **Mechanical Characteristics**

- Package: DFN1006-2 (1.0×0.6×0.5mm)
- Case Material: "Green" Molding Compound.
- Moisture Sensitivity: Level 1 per J-STD-020
- Marking Information: See Below

## **Applications**

- USB 2.0 and USB 3.0
- HDMI 1.3, HDMI 1.4 and HDMI 2.0
- SATA and e SATA interface
- DVI
- **IEEE 1394**
- Portable Electronics and Notebooks

# Marking Information



**3U** = Device Marking Code

# **Ordering Information**

Part Number	Shipping	Reel Size
PESDR3311P1A	10000/Tape & Reel	7 inch



# Absolute Maximum Ratings (TA=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit		
Peak Pulse Power (8/20µs)	Р <sub>РК</sub>	25	W		
Peak Pulse Current (8/20µs)	Ірр	3	A		
ESD per IEC 61000-4-2 (Air)	V	±15			
ESD per IEC 61000-4-2 (Contact)	Vesd	±15	- kV		
Lead temperature	TL	260	°C		
Operating Temperature Range	Тор	-40 ~ +85	°C		
Storage Temperature Range	Тѕтс	-55 ~ +150	C		

### Electrical Characteristics (TA=25°C unless otherwise specified)

Parameter	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Working Voltage	Vrwm			3.3	V	
Breakdown Voltage	V <sub>BR</sub>	7.0	11		V	I <sub>T</sub> = 1mA
Reverse Leakage Current	I <sub>R</sub>			0.1	μA	V <sub>RWM</sub> = 3.3V
Clamping voltage <sup>1)</sup>	V <sub>CL</sub>		9		V	$I_{PP} = 16A, t_p = 100ns$
Dynamic resistance1)	R <sub>DYN</sub>		0.3		Ω	
Clamping voltage <sup>2)</sup>	V <sub>CL</sub>		9		V	V <sub>ESD</sub> = 8kV
Clamping Voltage	Vc			8.5	V	I <sub>PP</sub> = 3A (8/20µs pulse)
Junction Capacitance	CJ		0.35		pF	$V_R = 0V, f = 1MHz$

Notes:

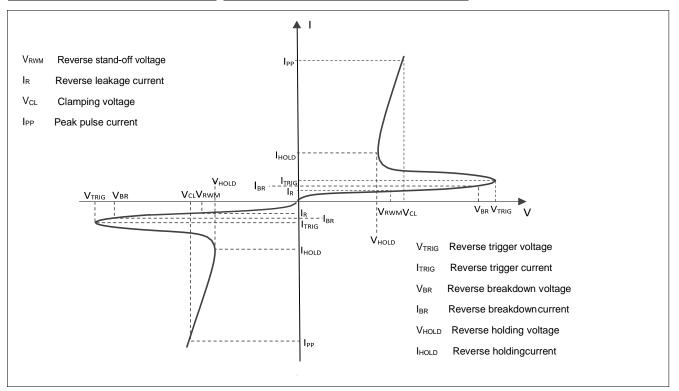
1) TLP parameter:  $Z_0 = 50\Omega$ , tp = 100 ns, tr = 2 ns, averaging window from 60 ns to 80 ns. RDYN is calculated from 4A to 16A.

2) Contact discharge mode, according to IEC61000-4-2.

3) Non-repetitive current pulse, according to IEC61000-4-5.



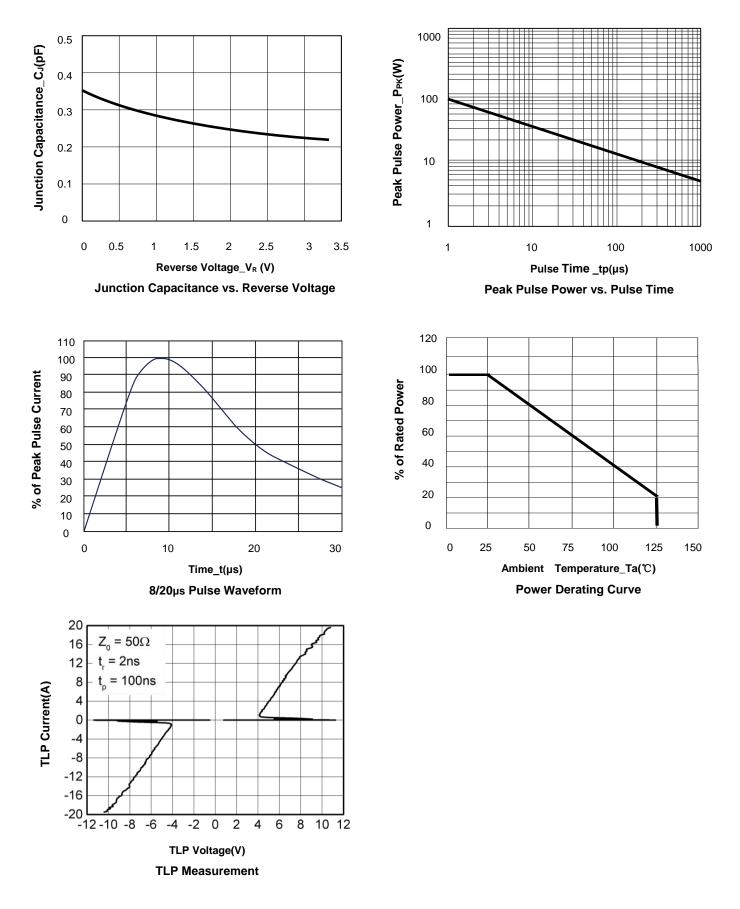
# Electrical characteristics ( $T_A = 25^{\circ}$ , unless otherwise noted)



Definitions of electrical characteristics

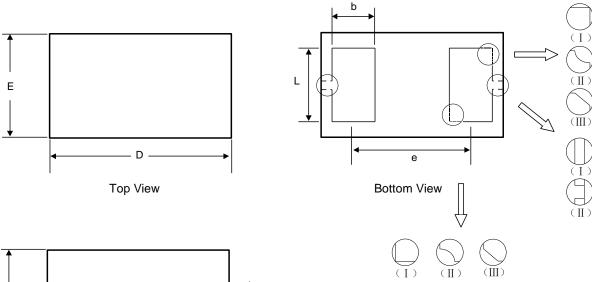


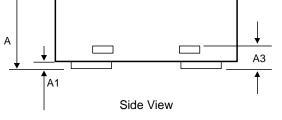
# Typical Performance Characteristics (T<sub>A</sub>=25°C unless otherwise Specified)





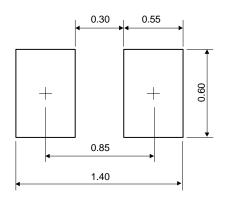
## DFN1006-2 Package Outline Drawing





Symbol		Dimensions in Millimeters			
	Min.	Тур.	Max.		
А	0.340	0.450	0.550		
Al	0.000	0.020	0.050		
A3		0.125 Ref.			
D	0.950	1.000	1.075		
E	0.490	0.600	0.675		
b	0.200	0.250	0.300		
L	0.450	0.500	0.550		
е		0.650 BSC			

#### Recommended PCB Layout (Unit: mm)



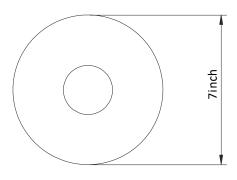
#### Notes:

This recommended land pattern is for reference purposes only. Please consult your manufacturing group to ensure your PCB design guidelines are met.

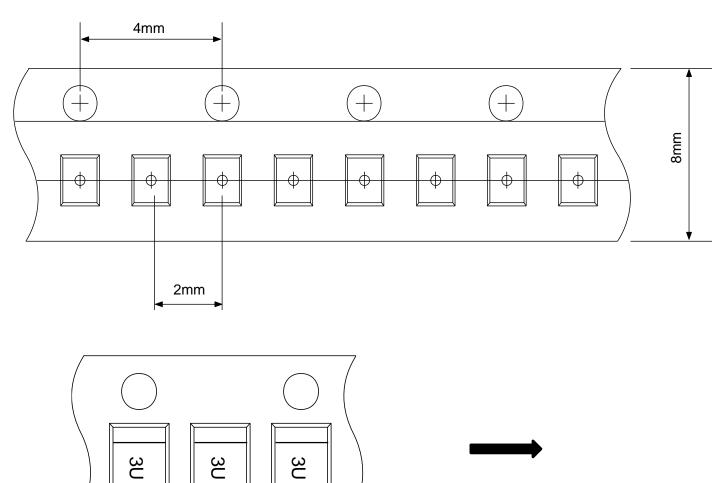


# TAPE AND REEL INFORMATION

#### **Reel Dimensions**



**Tape Dimensions** 



User Direction of Feed



# **IMPORTANT NOTICE**

The information given in this document is believed to be accurate and reliable but shall in no event be regarded as a guarantee of conditions or characteristics.PN-Silicon assumes no responsibility for any errors in this document, or for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of PN-Silicon.

The product listed in this document are designed to be used with ordinary electronic equipment or devices and are not authorized to used with equipment or devices which require an extremely high level of reliability and the malfunction of with would directly endanger human life (such as medical instruments, aerospace machinery, nuclear-reactor controllers, automotive and other safety device.)

The **PRESILCON** logo is a registered trademark of PN-Silicon co., ltd which reserves the right to make changes to the product or this document at any time without notice. Buyers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. PN-Silicon makes no warranty, representation or guarantee, express or implied, regarding the suitability of its products for any particular purpose. All rights reserved.